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| Substitute for form 1449A/PTO | | Complete if Known | | | | | | | | | | | | | | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i> | | <table border="1"> <tr> <td>Application Number</td> <td>Not yet assigned</td> </tr> <tr> <td>Filing Date</td> <td>June 28, 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Keeth et al.</td> </tr> <tr> <td>Group Art Unit</td> <td>Not yet assigned</td> </tr> <tr> <td>Examiner Name</td> <td>Not yet assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>DB000575-014</td> </tr> </table> | | | Application Number | Not yet assigned | Filing Date | June 28, 2001 | First Named Inventor | Keeth et al. | Group Art Unit | Not yet assigned | Examiner Name | Not yet assigned | Attorney Docket Number | DB000575-014 |
| Application Number | Not yet assigned | | | | | | | | | | | | | | | |
| Filing Date | June 28, 2001 | | | | | | | | | | | | | | | |
| First Named Inventor | Keeth et al. | | | | | | | | | | | | | | | |
| Group Art Unit | Not yet assigned | | | | | | | | | | | | | | | |
| Examiner Name | Not yet assigned | | | | | | | | | | | | | | | |
| Attorney Docket Number | DB000575-014 | | | | | | | | | | | | | | | |
| Sheet | 1 | of | 3 | | | | | | | | | | | | | |

U.S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | U.S. Patent Document | | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|--------------------|-----------------------|----------------------|-----------------------------------|---|--|---|
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| Examiner Signature | | Date Considered | |
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| | | | | Application Number | Not yet assigned |
| | | | | Filing Date | June 28, 2001 |
| | | | | First Named Inventor | Keeth et al. |
| | | | | Group Art Unit | Not yet assigned |
| | | | | Examiner Name | Not yet assigned |
| Sheet | 2 | of | 3 | Attorney Docket Number | DB000575-014 |

| OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS | | |
|---|-----------------------|---|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |
| | | SUGIBAYASHI ET AL., "A 30-ns 256-Mb DRAM with a Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993) |
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| | | U.S. Patent Application S.N. 08/194,184, entitled INTEGRATED CIRCUIT POWER SUPPLY HAVING PIECEWISE LINEARITY, filed 2/8/94 |

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will be taken as an indication that the
accompanying paper(s) was filed.

June 28, 2001

Applicant(s) Keeth et al.
Title 256 MEG DYNAMIC RANDOM *PTO/SB/05
ACCESS MEMORY *PTO/SB/17 x2
Pages of Spec. 232 No. of Claim 23 *Information Disclosure
Sheets of Drawings 367 Statement (PTO/SB/08A,
Declaration X Verified Stmt. -- 1 pg. and PTO/SB/08B,
Amt. of Check \$764 2 pgs.)
Atty's File No. DB000575-014 *copies of cited
ELP/CFO/mk references
*Preliminary Amendment
Including Deletion of
Inventors
Microfiche Appendix

The PTO did not receive the following
listed item(s) Claim 23